

Amendments to the Specification:

Please replace the paragraph beginning on page 8, line 22, with the following rewritten paragraph:

The blur correction apparatus is equipped with an angular speed sensor 101, a main CPU 102a and a blur correction CPU 102b, a voltage driver 103, a VCM 104, an optical position detection devices 105 and the like. The blur correction apparatus includes two control systems, i.e., a pitch control system and a yaw control system. Since these control systems adopt similar structures, the letters p and y are attached to indicate pitch and yaw in FIG. 1 to allow a single explanation to suffice with regard to the two control systems.

Please replace the paragraphs beginning on page 9, line 13, with the following rewritten paragraphs:

The blur correction CPU 102b executes processing for converting quantized outputs from the angular speed sensors 101p and 101y into target position information for a blur correcting lens 1. This target position information is input to VCMs (voice coil motors) 104p and 104y constituting an electromagnetic drive unit of the blur correction unit by using voltage drivers 103p and 103y so as to allow the VCMs 104p and 104y to move the blur correcting lens 1 for blur correction.

Optical position detection devices 105p and 105y detect the position of the blur correcting lens 1. The detected position of the blur correcting lens 1 is output to the blur correction CPU 102b to be used in drive control for the blur correcting lens 1.

Please replace the paragraph beginning on page 10, line 5, with the following rewritten paragraph:

FIG. 2 is a block diagram of the DSC having the blur correction apparatus according to the present invention mounted therein, as achieved in the embodiment. The DSC in the embodiment includes a liquid crystal monitor 13, an flash device 14, a pop-up drive unit 15, a shutter release-~~switch 16~~ switch 16, a zoom lever 17, an encoder 18, a zoom DC motor 19, a focus stepping motor 20, a CCD 21, a blur correction ON/OFF switch 22, the angular speed sensor 101, a main CPU 102a, a blur correction CPU 102b, driver circuits 103, 108 and 109, the VCM 104, the optical position detection devices 105, a filter 106, an EEPROM 107, lens groups L1 to L4 and the like. A still image obtained through a photographing operation is recorded into a recording unit such as a memory card (not shown).

Please replace the paragraph beginning on page 11, line 12, with the following rewritten paragraph:

The shutter release-~~switch 16~~ switch 16 and the zoom lever 17 are operating members used in a shutter release operation and a zooming operation respectively, and are both connected to the main CPU 102a. The encoder 18, which may be, for instance, a photointerrupter, detects the rotational angle of the zoom DC motor 19. Zoom encoder information output from the encoder 18 is input to a target position conversion unit 121 via a zoom control unit 123.

Please replace the paragraph beginning on page 12, line 17, with the following rewritten paragraph:

The main CPU 102a primarily executes camera sequence processing and processing related to image processing. The main CPU 102a is connected with the blur correction ON/OFF switch 22, the shutter release-~~switch 16~~ switch 16, the zoom lever 17, the liquid crystal monitor 13, the pop-up drive unit 15, the CCD 21 and the like.

Please replace the paragraph beginning on page 13, line 2, with the following rewritten paragraph:

It is to be noted that the main CPU 102a and the blur correction CPU 102b may together constitute ~~the one CPU-102~~.